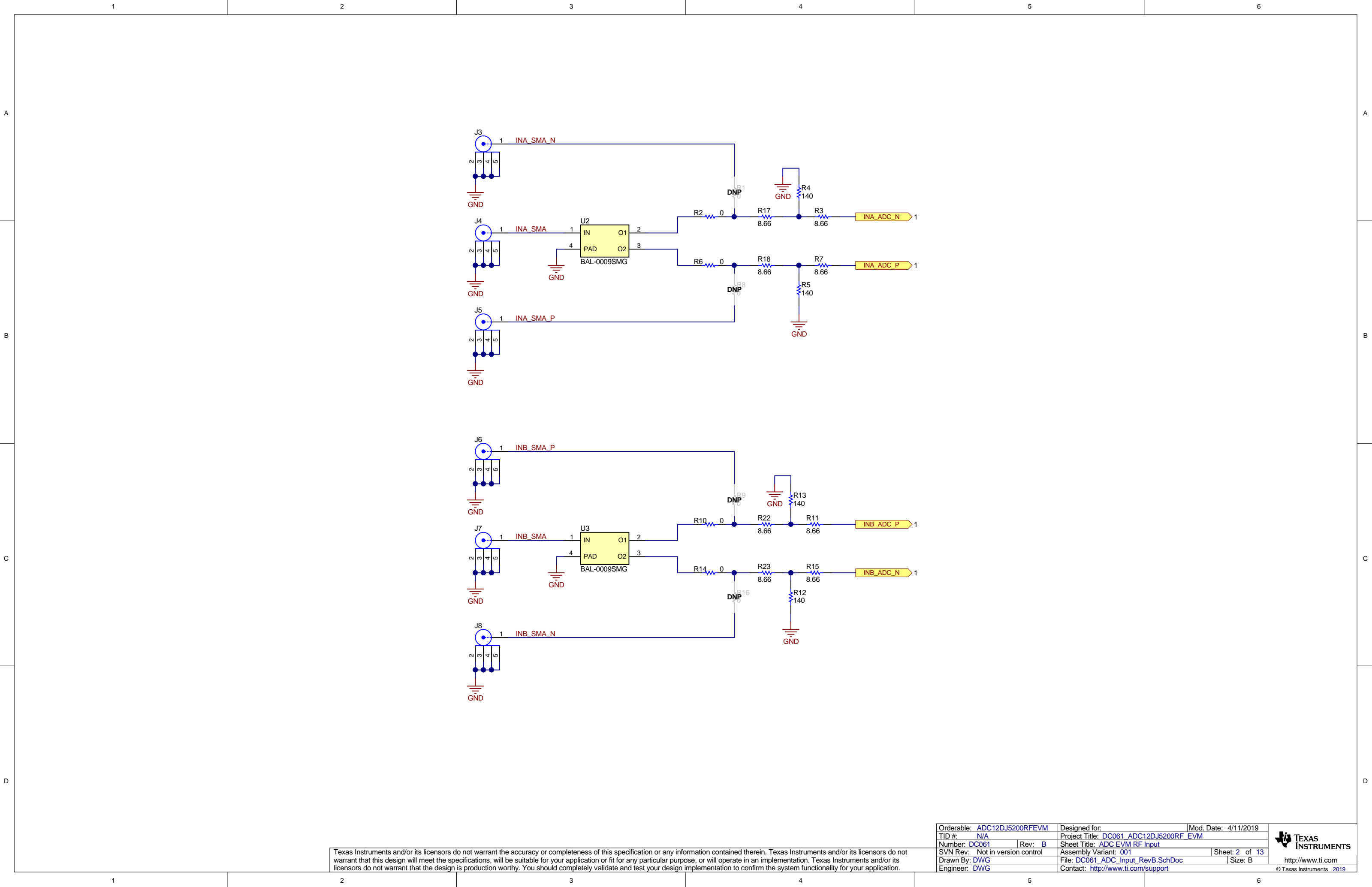
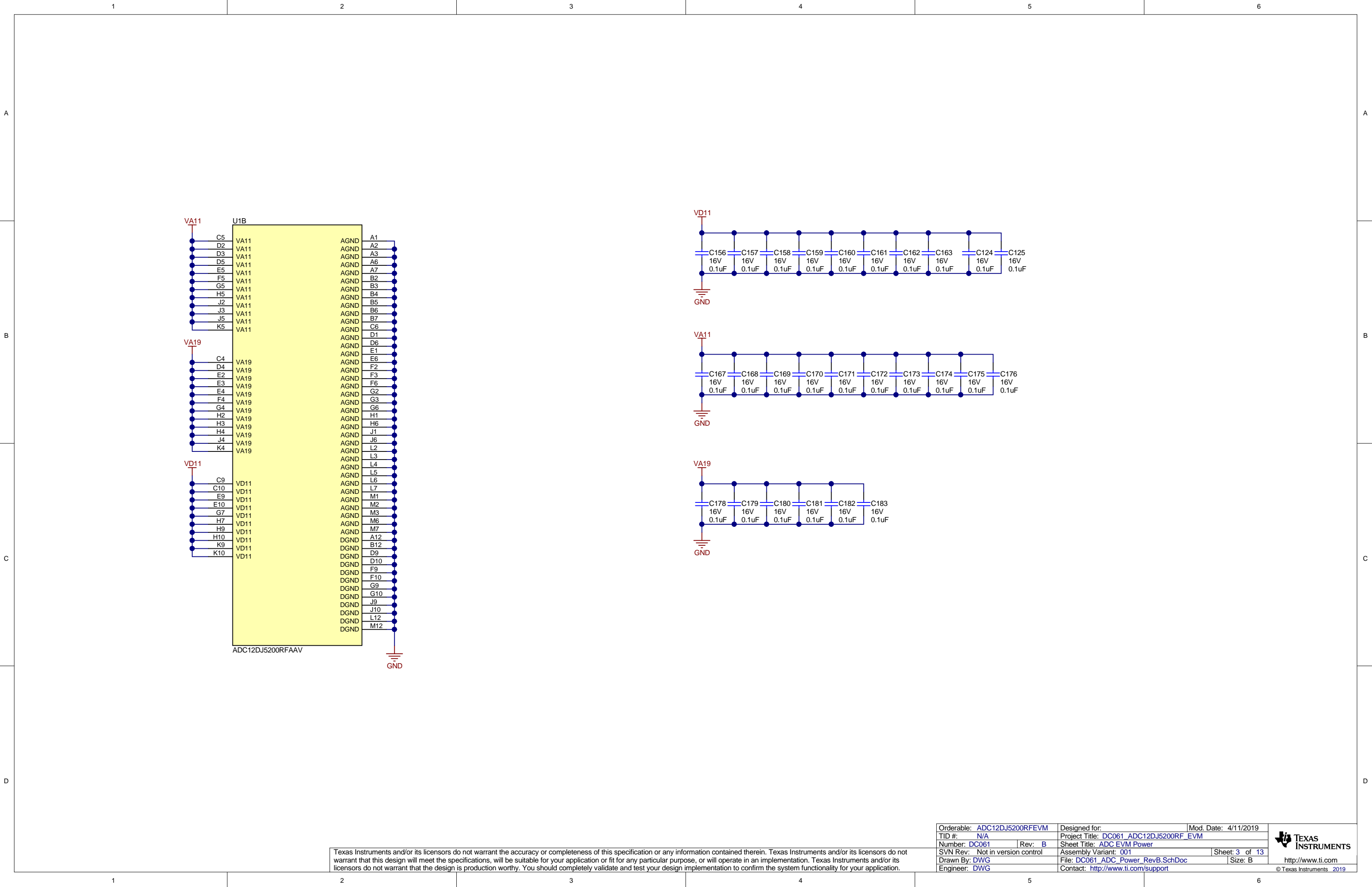


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Orderable: ADC12DJ5200RFEVM		Designed for:	Mod. Date: 4/11/2019
TID #: N/A		Project Title: DC061_ADC12DJ5200RF_EVM	
Number: DC061	Rev: B	Sheet Title: ADC EVM RF Input	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 2 of 13
Drawn By: DWG		File: DC061_ADC_Input_RevB.SchDoc	Size: B
Engineer: DWG		Contact: http://www.ti.com/support	

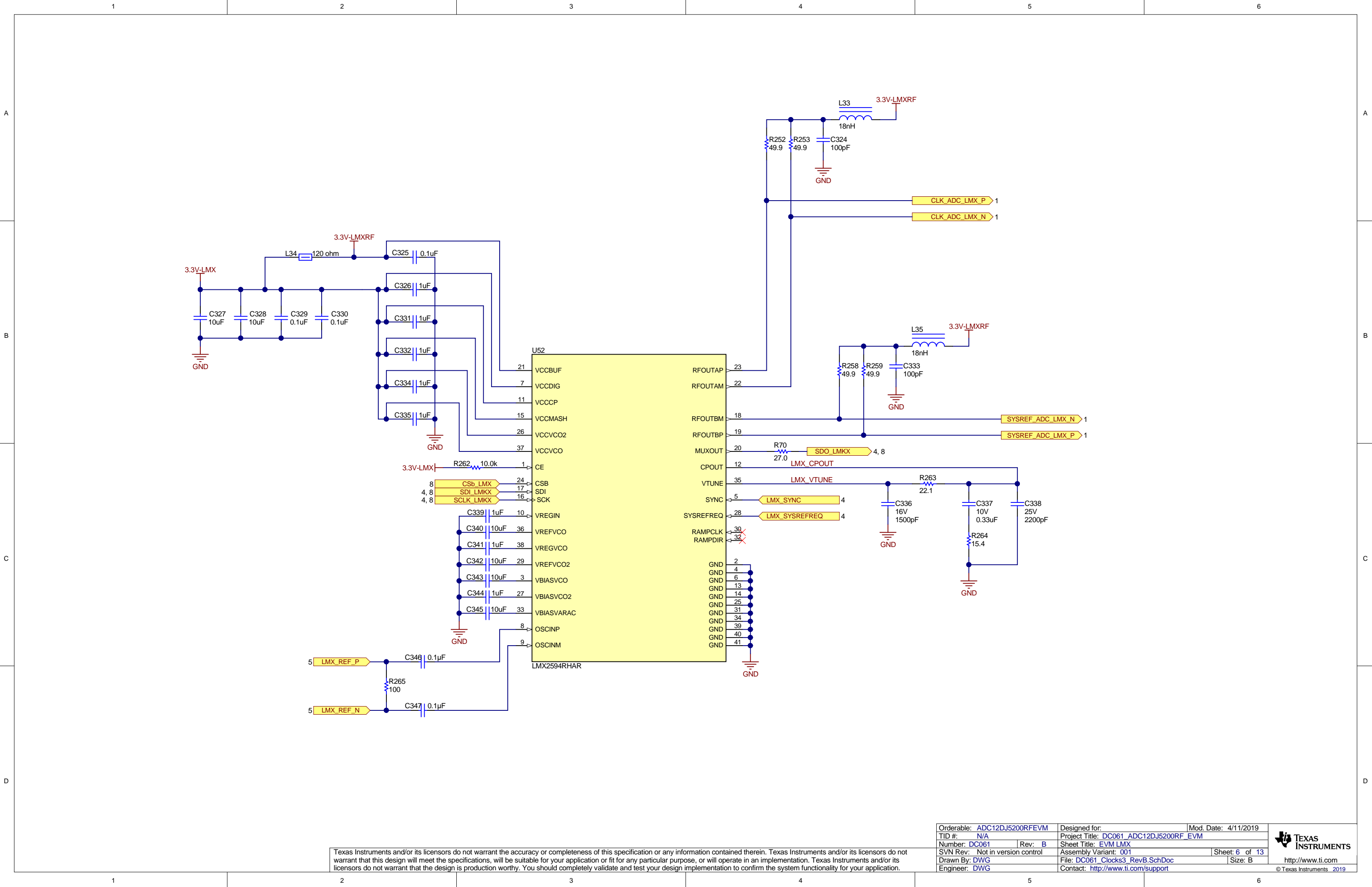


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Orderable: ADC12DJ5200RFEVM		Designed for:	Mod. Date: 4/11/2019
TID #:	N/A	Project Title: DC061_ADC12DJ5200RF_EVM	
Number: DC061	Rev: B	Sheet Title: ADC EVM Power	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 3 of 13
Drawn By: DWG		File: DC061_ADC_Power_RevB.SchDoc	Size: B
Engineer: DWG		Contact: http://www.ti.com/support	

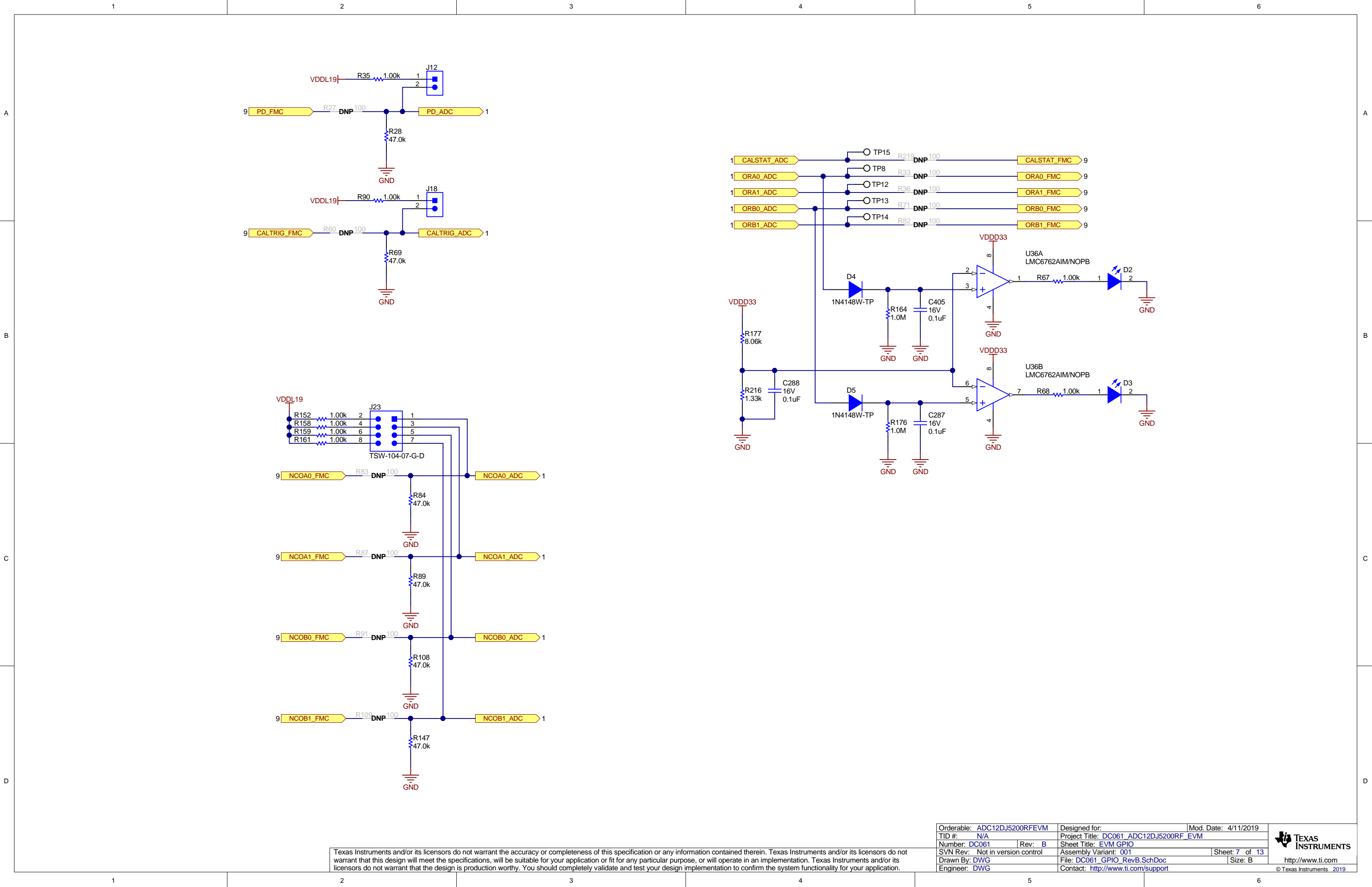
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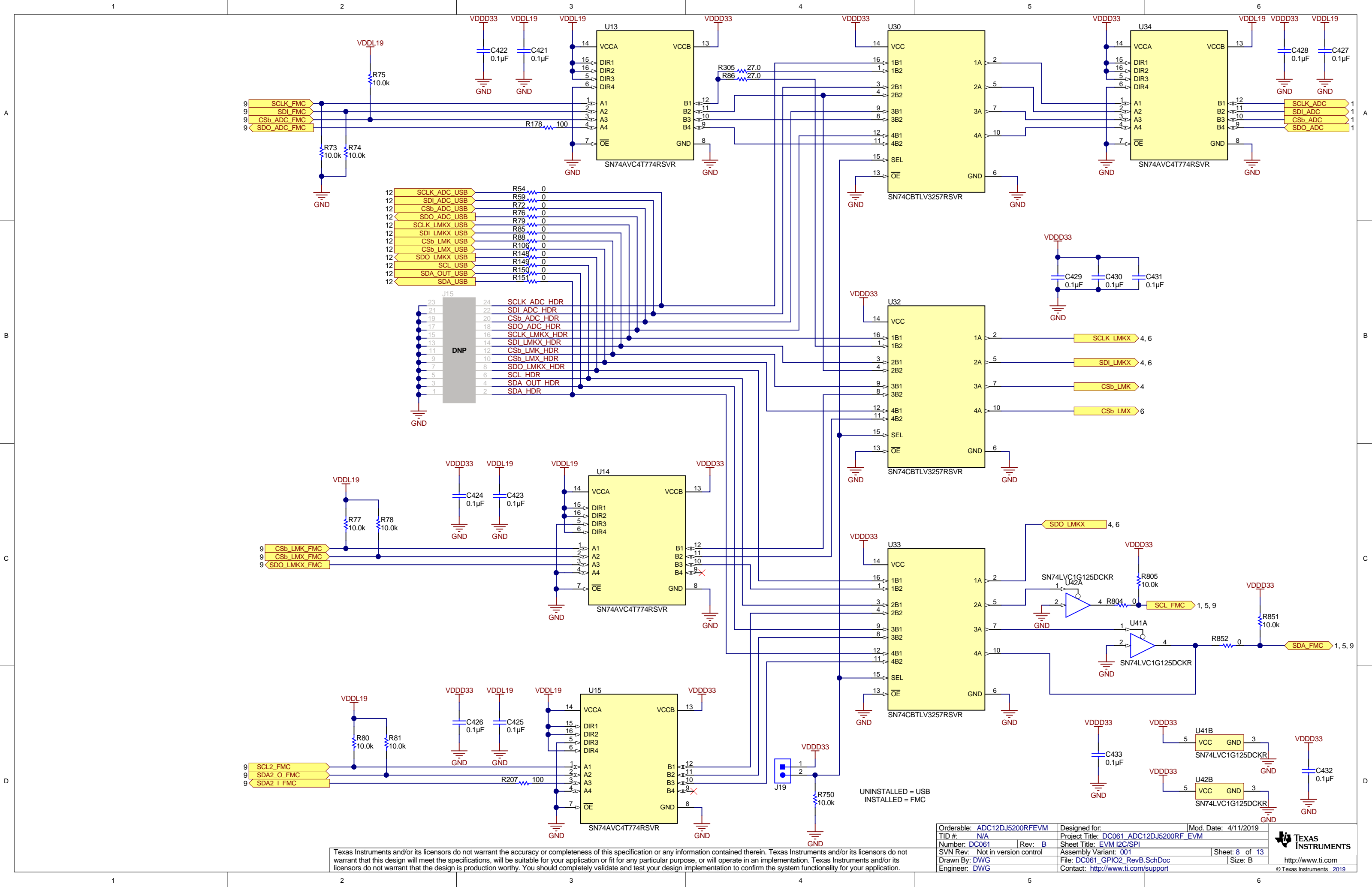
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Orderable: ADC12DJ5200RFEVM	Designed for:	Mod. Date: 4/11/2019
TID #: N/A	Project Title: DC061_ADC12DJ5200RF_EVM	
Number: DC061	Rev: B	Sheet Title: EVM LMX
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 6 of 13
Drawn By: DWG	File: DC061_Clocks3_RevB.SchDoc	Size: B
Engineer: DWG	Contact: http://www.ti.com/support	



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Orderable: ADC12DJ5200RFEVM	Designed for:	Mod. Date: 4/11/2019
TID #: N/A	Project Title: DC061_ADC12DJ5200RF_EVM	
Number: DC061	Rev: B	Sheet Title: EVM GPIO
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 7 of 13
Drawn By: DWG	File: DC061_GPIO_RevB.SchDoc	Size: B
Engineer: DWG	Contact: http://www.ti.com/support	

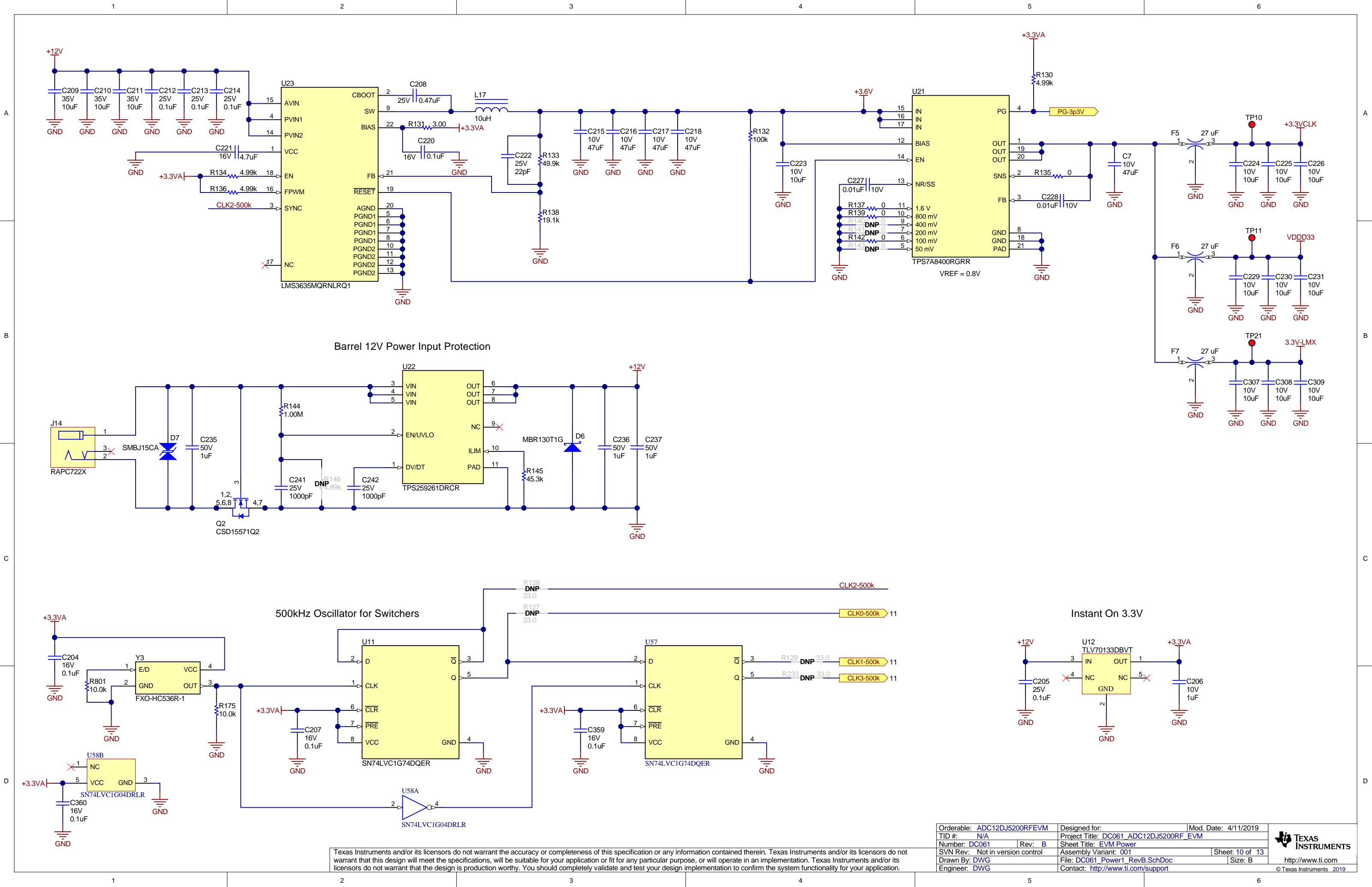


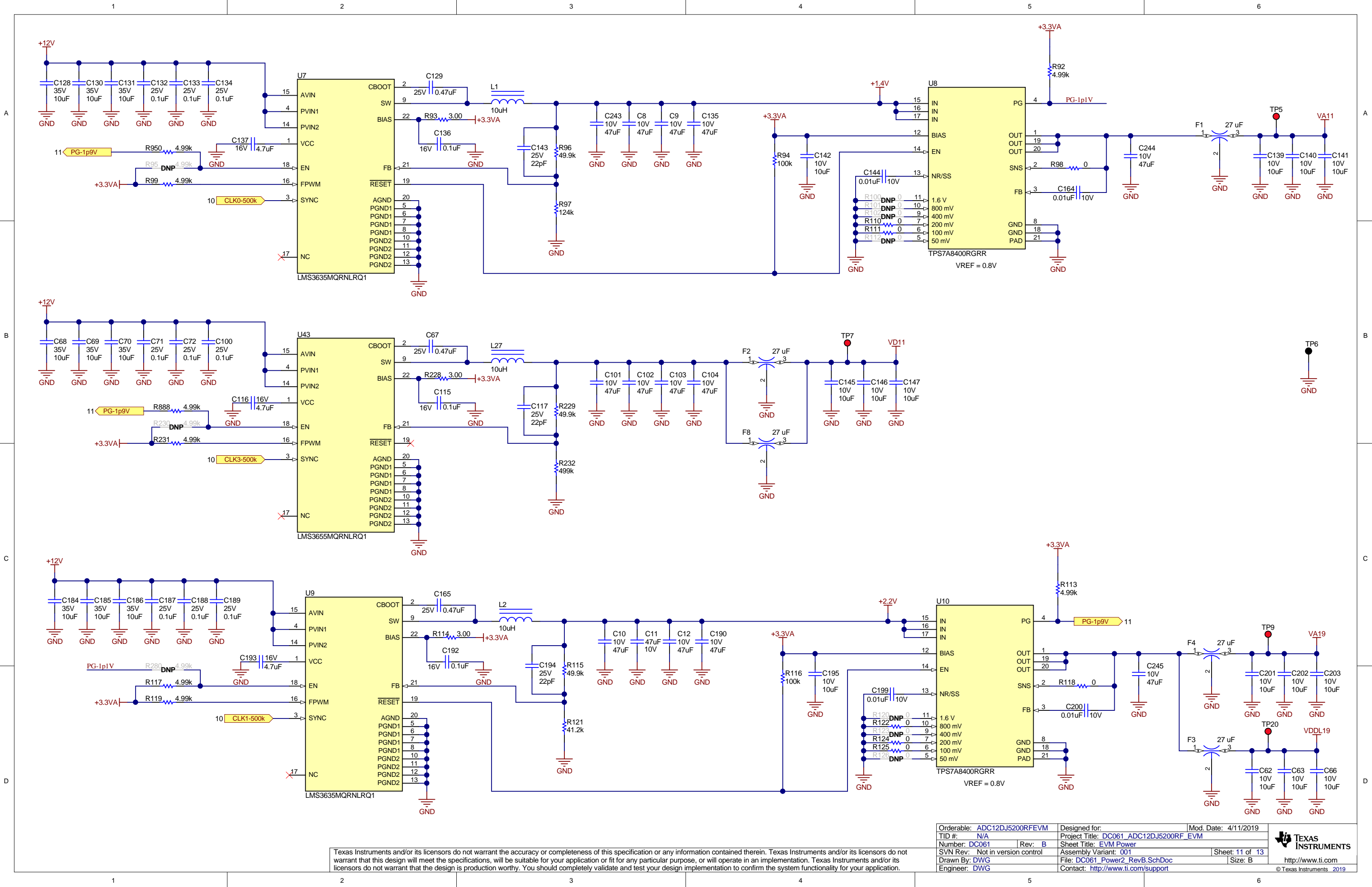
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Orderable: ADC12DJ5200RFEVM	Designed for:	Mod. Date: 4/11/2019
TID #: N/A	Project Title: DC061_ADC12DJ5200RF_EVM	
Number: DC061	Rev: B	Sheet Title: EVM I2C/SPI
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 8 of 13
Drawn By: DWG	File: DC061_GPIO2_RevB.SchDoc	Size: B
Engineer: DWG	Contact: http://www.ti.com/support	

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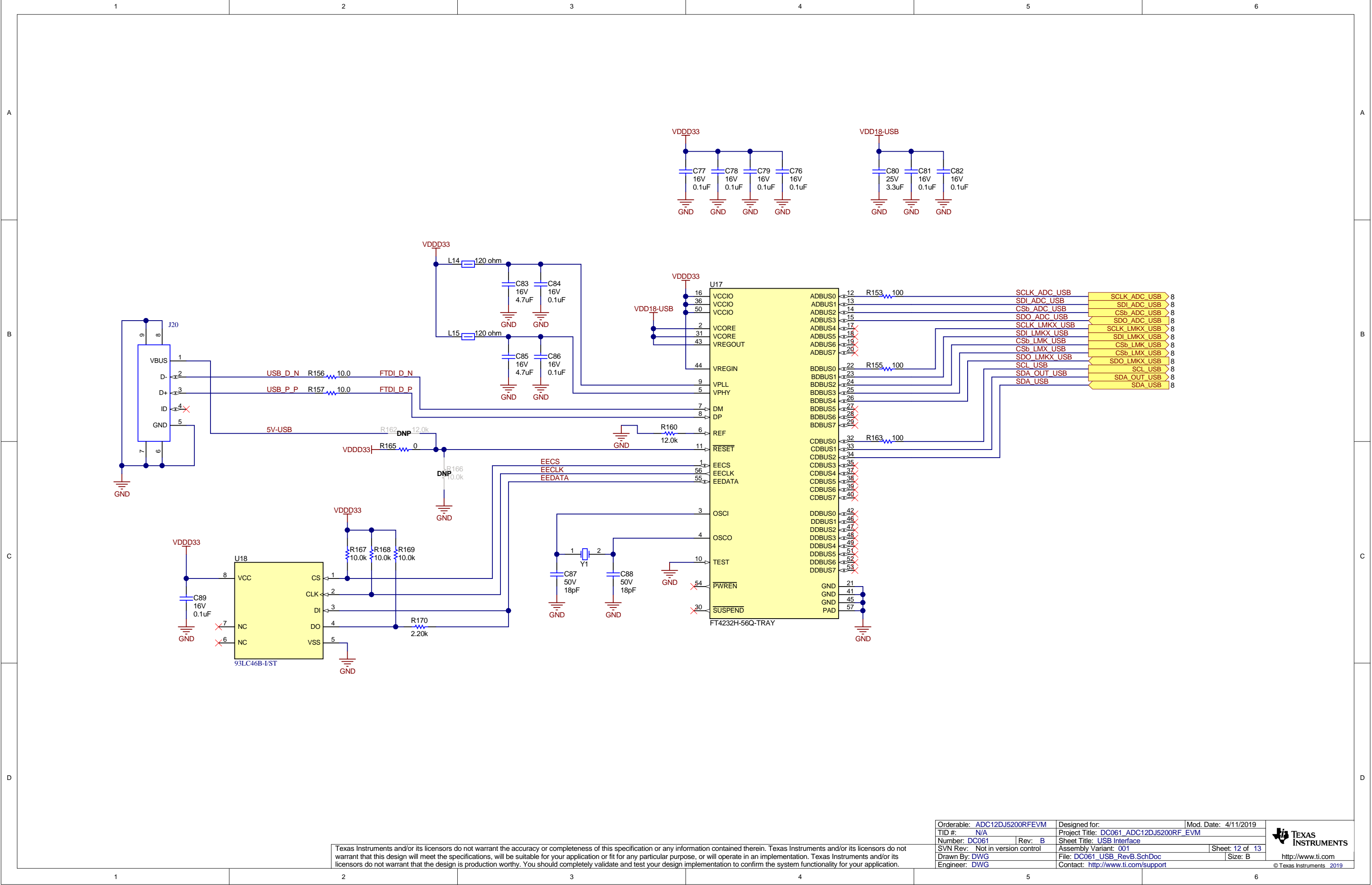


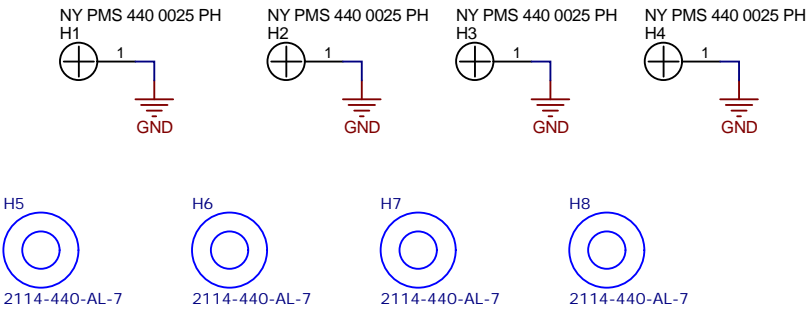




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Orderable: ADC12DJ5200RFEVM	Designed for:	Mod. Date: 4/11/2019
TID #: N/A	Project Title: DC061_ADC12DJ5200RF_EVM	
Number: DC061	Rev: B	Sheet Title: EVM Power
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 11 of 13
Drawn By: DWG	File: DC061_Power2_RevB.SchDoc	Size: B
Engineer: DWG	Contact: http://www.ti.com/support	





PCB Number: DC061

PCB Rev: B

DNP

FID1

DNP

FID4

DNP

FID2

DNP

FID5

DNP

FID3

DNP

FID6

PCB

LOGO

Texas Instruments



PCB

LOGO

FCC disclaimer

PCB

LOGO

WEEE logo



Variant/Label Table	

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Orderable: ADC12DJ5200RFEVM		Designed for:	Mod. Date: 6/11/2019
TID #: N/A		Project Title: DC061_ADC12DJ5200RF_EVM	
Number: DC061	Rev: B	Sheet Title: EVM Misc	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 13 of 13	
Drawn By: DWG	File: DC061_Misc_RevB.SchDoc	Size: B	
Engineer: DWG	Contact: http://www.ti.com/support		